Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	65	@ad<"20010824" and (ternary adj content adj addressable adj memory)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/17 19:03
S2	7	@ad<"20010824" and (ternary adj content adj addressable adj memory) and (CPU or ALU) and (match$3) and address and network and IP	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/17 19:33
S3	6	@ad<"20010824" and (ternary adj content adj addressable adj memory) and (CPU or ALU) and (match$3) and address and network and IP and classif$7	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/17 19:08
S4	0	@ad<"20010824" and (ternary adj content adj addressable adj memory) and (CPU or ALU) and (match$3) and address and IP and classif$7 and (stuff or unstuff)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/17 19:09
S5	0	@ad<"20010824" and (ternary adj content adj addressable adj memory) same (CPU or ALU) and (lookup near address near IP) and ((classif$7 or process$3 or match$3) near packet)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/17 19:10
S6	0	@ad<"20010824" and (ternary adj content adj addressable adj memory) same (CPU or ALU) and (lookup same address same IP) and ((classif$7 or process$3 or match$3)same packet)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/17 19:11
S7	4	@ad<"20010824" and (ternary adj content adj addressable adj memory) and (CPU or ALU) and (lookup same address same IP) and ((classif$7 or process$3 or match$3)same packet)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/17 19:14
S8	11	@ad<"20010824" and (ternary adj content adj addressable adj memory) and (CPU or ALU or TCAM) and (lookup same address) and ((classif$7 or process$3 or match$3) same packet)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/17 19:19
S9	0	@ad<"20010824" and ((ternary adj content adj addressable adj memory) or TCAM) and (CPU or (central adj processing adj unit)) and (ALU or (arithmetic adj logic adj unit)) and (lookup same address)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/17 19:20
S10	0	@ad<"20010824" and ((ternary adj content adj addressable adj memory) or TCAM) and (CPU or (central adj processing adj unit)) and (ALU or (arithmetic adj logic adj unit))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/17 19:20

S11	21	@ad<"20010824" and ((ternary adj content adj addressable adj memory) or TCAM) and (CPU or (central adj processing adj unit) or ALU or (arithmetic adj logic adj unit))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/20 20:46
S12	2	@ad<"20010824" and ((ternary adj content adj addressable adj memory) or TCAM) same (CPU or (central adj processing adj unit) or ALU or (arithmetic adj logic adj unit))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/17 19:21
S18	0	@ad<"20010824" and ((ternary adj content adj addressable adj memory) or TCAM) with (ALU or (arithmetic adj logic adj unit))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/20 20:47
S19	0	@ad<"20010824" and ((ternary adj content adj addressable adj memory) or TCAM) same (ALU or (arithmetic adj logic adj unit))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/22 18:09
S20	51	@ad<"20010824" and ((content adj addressable adj memory) or TCAM or CAM) same (ALU or (arithmetic adj logic adj unit))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/20 20:49
S21	21	@ad<"20010824" and ((content adj addressable adj memory) or TCAM or CAM) with (ALU or (arithmetic adj logic adj unit))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/20 20:57
S22	51	@ad<"20010824" and ((content adj addressable adj memory) or TCAM or CAM) same (ALU or (arithmetic adj logic adj unit))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/20 20:50
S23	4	@ad<"20010824" and ((content adj addressable adj memory) or TCAM or CAM) with (ALU or (arithmetic adj logic adj unit)) same (within or inside)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/21 19:20
S24	35	@ad<"20010824" and ((content adj addressable adj memory) or TCAM or CAM) with (ALU or ((arithmetic or logic) adj unit)) same (within or inside)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/21 19:21
S25	5	@ad<"20010824" and ((content adj addressable adj memory) or TCAM or CAM) with (ALU or ((arithmetic or logic) adj unit)) with (within or inside)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/21 19:21
S26	5	@ad<"20000919" and ((content adj addressable adj memory) or TCAM or CAM) with (ALU or ((arithmetic or logic) adj unit)) with (within or inside)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/21 19:26

S27	30	@ad<"20000919" and ((content adj addressable adj memory) or TCAM or CAM) with (ALU or ((arithmetic or logic) adj unit)) same (within or inside)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/21 20:13
S28	43	@ad<"20000919" and ((content adj addressable adj memory) or TCAM or CAM) same (ALU or ((arithmetic or logic) adj unit)) same (within or inside)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/21 20:42
S29	30	@ad<"20000919" and ((content adj addressable adj memory) or TCAM or CAM) with (ALU or ((arithmetic or logic) adj unit)) same (within or inside or embed$4)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/21 20:47
S30	5	@ad<"20000919" and ((content adj addressable adj memory) or TCAM or CAM) with (ALU or ((arithmetic or logic) adj unit)) with (within or inside or embed$4)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/21 20:43
S31	8	@ad<"20000919" and ((content adj addressable adj memory) or TCAM or CAM) near (ALU or ((arithmetic or logic) adj unit)) and (within or inside or embed$4)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/21 20:50
S32	0	@ad<"20000919" and ((content adj addressable adj memory) or TCAM or CAM) near (ALU or ((arithmetic or logic) adj unit)) same (within or inside or embed$4 or in or compris$3 or contain$3) same (CPU or (process$3 adj unit))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/21 20:51
S33	1	@ad<"20000919" and ((content adj addressable adj memory) or TCAM or CAM) near (ALU or ((arithmetic or logic) adj unit)) and (within or inside or embed$4 or in or compris$3 or contain$3) same (CPU or (process$3 adj unit))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/22 16:45
S34	23	@ad<"20000919" and ((content adj addressable adj memory) or TCAM or CAM) with (ALU or ((arithmetic or logic) adj unit)) and (within or inside or embed$4 or in or compris$3 or contain$3) same (CPU or (process$3 adj unit))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/21 21:03
S35	1	@ad<"20000919" and ((content adj addressable adj memory) or TCAM or CAM) with (ALU or ((arithmetic or logic) adj unit)) same (within or inside or embed$4 or in or compris$3 or contain$3) same (CPU or (process$3 adj unit))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/21 20:55

S36	13	@ad<"20000919" and ((content adj address$4 adj memory) or TCAM or CAM) same (ALU or ((arithmetic or logic) near unit)) same (within or inside or embed$4 or in or compris$3 or contain$3) same (CPU or (process$3 adj unit))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/21 21:12
S37	22	@ad<"20000919" and ((content adj address$4 adj memory) or TCAM or CAM) same (ALU or ((arithmetic or logic) near unit)) same (CPU or (process$3 adj unit))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/21 21:12
S38	22	@ad<"20000919" and ((content adj address$4 adj memory) or TCAM or CAM) same (ALU or ((arithmetic or logic) near unit)) same (CPU or (process$3 near unit))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/21 21:12
S39	62	@ad<"20000919" and ((content adj address$4 adj memory) or TCAM or CAM) same (ALU or ((arithmetic or logic) near unit)) and (within or inside or embed$4 or in or compris$3 or contain$3) same (CPU or (process$3 adj unit))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/21 21:12
S40	23	@ad<"20000919" and ((content adj address$4 adj memory) or TCAM or CAM) same (ALU or ((arithmetic or logic) near unit)) and (within or inside or embed$4 or in or compris$3 or contain$3) near (CPU or (process$3 adj unit))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/21 21:14
S41	2875	@ad<"20000919" and ((content adj address$4 adj memory) or TCAM or CAM or ALU or ((arithmetic or logic) near unit)) and (within or inside or embed$4 or in or compris$3 or contain$3) near (CPU or (process$3 adj unit))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/21 21:14
S42	283	@ad<"20000919" and ((content adj address$4 adj memory) or TCAM or CAM or ALU or ((arithmetic or logic) near unit)) with (within or inside or embed$4 or in or compris$3 or contain$3) near (CPU or (process$3 adj unit))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/21 21:15
S43	37	@ad<"20000919" and ((content adj address$4 adj memory) or TCAM or CAM or ALU or ((arithmetic or logic) near unit)) near (within or inside or embed$4 or in or compris$3 or contain$3) near (CPU or (process$3 adj unit))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/21 21:17

S44	35	@ad<"20000919" and ((content adj address$4 adj memory) or TCAM or CAM or ALU or ((arithmetic adj logic) adj unit)) near (within or inside or embed$4 or in or compris$3 or contain$3) near (CPU or (process$3 adj unit))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/21 21:20
S45	0	@ad<"20000919" and ((content adj address$4 adj memory) or TCAM or CAM) near (within or inside or embed$4 or in or compris$3 or contain$3) near (CPU or (process$3 adj unit))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/21 21:21
S46	27	@ad<"20000919" and ((content adj address$4 adj memory) or TCAM or CAM) with (within or inside or embed$4 or in or compris$3 or contain$3) near (CPU or (process$3 adj unit))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/21 21:22
S47	209	@ad<"20000919" and ((content adj address$4 adj memory) or TCAM or CAM) with (within or inside or embed$4 or in or compris$3 or contain$3) with (CPU or (process$3 adj unit))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/21 21:22
S48	13	@ad<"20000919" and ((content adj address$4 adj memory) or TCAM) with (within or inside or embed$4 or in or compris$3 or contain$3) with (CPU or (process$3 adj unit))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/21 21:27
S49	0	@ad<"20000919" and ((ternary adj content adj address$4 adj memory) or TCAM) with (within or inside or embed$4 or in or compris$3 or contain$3) with (CPU or (process$3 adj unit))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/21 21:27
S50	1	@ad<"20000919" and ((ternary adj content adj address$4 adj memory) or TCAM)same (within or inside or embed$4 or in or compris$3 or contain$3) with (CPU or (process$3 adj unit))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/21 21:27
S51	1	@ad<"20000919" and ((ternary adj content adj address$4 adj memory) or TCAM) same (within or inside or embed$4 or in or compris$3 or contain$3) with (CPU or (process$3 adj unit))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/21 21:35
S52	0	@ad<"20000919" and ((ternary adj content adj address$4 adj memory) or TCAM) near (CPU or (process$3 adj unit))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/21 21:36

S53	1	@ad<"20000919" and ((ternary adj content adj address$4 adj memory) or TCAM) with (CPU or (process$3 adj unit))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/21 21:36
S54	1	@ad<"20000919" and ((ternary adj content adj address$4 adj memory) or TCAM) same (CPU or (process$3 adj unit))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/21 21:36
S55	17	@ad<"20000919" and ((ternary adj content adj address$4 adj memory) or TCAM) and (CPU or (process$3 adj unit))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/21 21:45
S56	21	@ad<"20000919" and ((ternary adj content adj address$4 adj memory) or TCAM) and (CPU or ((process$3 or control) near unit))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/21 21:48
S57	326	@ad<"20000919" and ((content adj address$4 adj memory) or TCAM) same (CPU or ((process$3 or control) near unit))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/21 21:49
S58	7	@ad<"20000919" and ((content adj address$4 adj memory) or TCAM) near (CPU or ((process$3 or control) near unit))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/21 21:49
S59	35	@ad<"20000919" and (ALU or ((arithmetic adj logic) adj unit)) near (within or inside or embed$4 or in or compris$3 or contain$3) near (CPU or (process$3 adj unit))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/22 16:46
S60	0	@ad<"20000919" and (ALU or ((arithmetic adj logic) adj unit)) near (within or inside or embed$4 or in or compris$3 or contain$3) near (CPU or (process$3 adj unit)) near memory	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/22 16:47
S61	18	@ad<"20000919" and (ALU or ((arithmetic adj logic) adj unit)) near (within or inside or embed$4 or in or compris$3 or contain$3) near (CPU or (process$3 adj unit)) same (TCAM or CAM or memory)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/22 16:47
S65	0	@ad<"20010824" and ((ternary adj content adj addressable adj memory) or TCAM or (ternary adj CAM)) same (ALU or (arithmetic adj logic adj unit))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/22 18:10
S66	2	@ad<"20010824" and ((ternary adj content adj addressable adj memory) or TCAM or (ternary adj CAM)) with (CPU or (process$3 adj unit) or ALU or (arithmetic adj logic adj unit))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/22 18:11

S67	2	@ad<"20000919" and ((ternary adj content adj addressable adj memory) or TCAM or (ternary adj CAM)) same (CPU or (process$3 adj unit) or ALU or (arithmetic adj logic adj unit))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/22 18:21
S68	41	@ad<"20000919" and ((ternary adj content adj address$4 adj memory) or TCAM or (ternary adj CAM)) same (CPU or (process$3) or ALU or ((arithmetic or logic) adj unit))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/22 19:34
S69	27	@ad<"20000919" and ((ternary adj content adj address$4 adj memory) or TCAM or (ternary adj CAM)) with (CPU or (process$3) or ALU or ((arithmetic or logic) adj unit))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/22 18:23
S71	12	@ad<"20000919" and ((ternary adj content adj address$4 adj memory) or TCAM or (ternary adj CAM) or CAM) near (within or compris$3 or contain$3 or in or embed$4) near (CPU or (process$3) or ALU or ((arithmetic or logic)))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/22 19:38
S72	21	@ad<"20000919" and ((ternary adj content adj address$4 adj memory) or TCAM or (ternary adj CAM) or CAM) near (within or compris$3 or contain$3 or in or embed$4 or includ$3) near (CPU or (process$3) or ALU or ((arithmetic or logic)))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/23 14:51
S86	13	@ad<"20000919" and ((ternary adj content adj address$4 adj memory) or TCAM or (ternary adj CAM) or CAM) near (within or compris$3 or contain$3 or in or embed$4 or includ$3) near (ALU or ((arithmetic or logic)))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/23 14:55
S87	96	@ad<"20000919" and ((ternary adj content adj address$4 adj memory) or TCAM or (ternary adj CAM) or CAM) with (within or compris$3 or contain$3 or in or embed$4 or includ$3) near (ALU or ((arithmetic or logic)))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/23 14:56
S88	1	@ad<"20000919" and ((ternary adj content adj address$4 adj memory) or TCAM or (ternary adj CAM) or CAM) with ((locat$3 near within) or compris$3 or contain$3 or in or embed$4 or includ$3) near (ALU or ((arithmetic or logic) adj unit))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/01/25 17:22

S98	10	((ternary adj content adj address$4 adj memory) or TCAM or (ternary adj CAM) or CAM) with ((locat$3 near within) or compris$3 or contain$3 or in or embed$4 or includ$3) near (ALU or ((arithmetic or logic) adj unit))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/09/15 20:20
S99	0	((ternary adj content adj address$4 adj memory) or TCAM or (ternary adj CAM) or CAM) with ((locat$3 near within) or compris$3 or contain$3 or in or embed$4 or includ$3 or coupl$3) near (ALU or ((arithmetic or logic) adj unit)) same CPU	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/09/15 20:16
S10 0	3	((ternary adj content adj address$4 adj memory) or TCAM or (ternary adj CAM) or CAM) with ((locat$3 near within) or compris$3 or contain$3 or in or embed$4 or includ$3 or coupl$3) near (ALU or ((arithmetic or logic) adj unit)) and CPU	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/09/15 20:16
S10 1	0	((ternary adj content adj address$4 adj memory) or TCAM or (ternary adj CAM) or CAM) with ((locat$3 near within) or compris$3 or contain$3 or in or embed$4 or includ$3 or coupl$3) near (ALU or ((arithmetic or logic) adj unit)) same (CPU or processor)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/09/15 20:16
S10 2	0	((ternary adj content adj address$4 adj memory) or TCAM or (ternary adj CAM)) with ((locat$3 near within) or compris$3 or contain$3 or in or embed$4 or includ$3) with (ALU or ((arithmetic or logic) adj unit))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/09/15 20:20
S10 3	0	((ternary adj content adj address$4 adj memory) or TCAM or (ternary adj CAM)) with (locat$3 or compris$3 or contain$3 or in or embed$4 or includ$3 or coupl$3) with (ALU or ((arithmetic or logic) adj unit))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/09/15 20:21
S10 4	0	((ternary adj content adj address$4 adj memory) or TCAM or (ternary adj CAM)) same (locat$3 or compris$3 or contain$3 or in or embed$4 or includ$3 or coupl$3) with (ALU or ((arithmetic or logic) adj unit))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/09/15 20:21
S10 5	2	((ternary adj content adj address$4 adj memory) or TCAM or (ternary adj CAM)) same (locat$3 or compris$3 or contain$3 or in or embed$4 or includ$3 or coupl$3) same (ALU or ((arithmetic or logic) adj unit))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/09/15 20:22

S106	0	((ternary adj content adj address$4 adj memory) or TCAM or (ternary adj CAM)) same (ALU or ((arithmetic or logic) adj unit)) same CPU	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/09/15 20:23
S107	3	((ternary adj content adj address$4 adj memory) or TCAM or (ternary adj CAM)) same (ALU or ((arithmetic or logic or processing) adj unit)) same CPU	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/09/15 20:31
S108	11	((ternary adj content adj address$4 adj memory) or TCAM or (ternary adj CAM)) same (CPU or ALU or ((arithmetic or logic or processing) adj unit))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/09/15 20:27
S109	44	((ternary adj content adj address$4 adj memory) or TCAM or (ternary adj CAM)) and (ALU or ((arithmetic or logic or processing) adj unit)) same CPU	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/09/15 20:31
S110	1	@ad<"20000919" and ((ternary adj content adj address$4 adj memory) or TCAM or (ternary adj CAM) or CAM) with ("near" or within or compris$3 or contain$3 or in or embed$4 or includ$3) near (ALU or ((arithmetic or logic) adj unit))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/01/25 17:23
S111	2	@ad<"20000919" and ((ternary adj content adj address$4 adj memory) or TCAM or (ternary adj CAM) or CAM) with (inside or within or connect$3 or compris$3 or contain$3 or in or embed$4 or includ$3) near (ALU or ((arithmetic or logic) adj unit))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/01/25 17:23
S112	28	@ad<"20000919" and ((ternary adj content adj address$4 adj memory) or TCAM or (ternary adj CAM) or CAM) with (inside or within or connect$3 or compris$3 or contain$3 or in or embed$4 or includ$3) with (ALU or ((arithmetic or logic) adj unit))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/01/25 17:31
S113	0	@ad<"20000919" and ((ternary adj content adj address$4 adj memory) or TCAM or (ternary adj CAM)) with (ALU or ((arithmetic or logic) adj unit))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/01/25 17:32
S114	0	@ad<"20000919" and ((ternary adj content adj address$4 adj memory) or TCAM or (ternary adj CAM)) same (ALU or ((arithmetic or logic) adj unit))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/01/25 17:32

S11 5	2	@ad<"20000919" and ((ternary adj content adj address$4 adj memory) or TCAM or (ternary adj CAM)) same (CPU or ALU or ((arithmetic or logic) adj unit))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/01/25 17:50
S11 6	16	@ad<"20000919" and ((ternary adj content adj address$4 adj memory) or TCAM or (ternary adj CAM)) same (CPU or ALU or ((arithmetic or logic) adj unit) or microprocessor or processor)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/01/25 18:07
S11 7	16	@ad<"20000919" and ((ternary adj content adj address$4 adj memory) or TCAM or (ternary adj CAM)) same (CPU or ALU or ((arithmetic or logic or processing) adj unit) or microprocessor or processor)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/01/25 18:30
S11 8	124	((ternary adj content adj address$4 adj memory) or TCAM or (ternary adj CAM)) same (CPU or ALU or ((arithmetic or logic or processing) adj unit) or microprocessor or processor)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/01/25 18:07
S11 9	80	((ternary adj content adj address$4 adj memory) or TCAM or (ternary adj CAM)) with (CPU or ALU or ((arithmetic or logic or processing) adj unit) or microprocessor or processor)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/01/25 18:07
S12 0	4	"09406170"	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/01/25 18:19
S12 1	4831	@ad<"20000919" and ((ternary adj content adj address$4 adj memory) or TCAM or (ternary adj CAM) or CAM) same (CPU or ALU or ((arithmetic or logic or processing) adj unit) or microprocessor or processor)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/01/25 18:30
S12 2	2455	@ad<"20000919" and ((ternary adj content adj address$4 adj memory) or TCAM or (ternary adj CAM) or CAM) with (CPU or ALU or ((arithmetic or logic or processing) adj unit) or microprocessor or processor)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/01/25 18:31
S12 3	61	@ad<"20000919" and ((ternary adj content adj address$4 adj memory) or TCAM or (ternary adj CAM) or CAM) with (ALU or ((arithmetic or logic or processing) adj unit)) with (CPU or microprocessor or processor)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/01/25 19:35

S124	2	"6324087".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/01/25 19:24
S125	2	"6324087".pn. and (CAM or TCAM)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/01/25 19:24
S126	0	"6324087".pn. and (CAM or TCAM) near logic	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/01/25 19:25
S127	0	"6324087".pn. and (CAM or TCAM) near (processor or logic or DSP)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/01/25 19:25
S128	0	@ad<"20000919" and ((ternary adj content adj address$4 adj memory) or TCAM or (ternary adj CAM)) with (ALU or ((arithmetic or logic or processing) adj unit))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/01/25 19:36
S129	2	@ad<"20000919" and ((ternary adj content adj address$4 adj memory) or TCAM or (ternary adj CAM)) same (ALU or ((arithmetic or logic or processing) adj unit))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/01/25 19:37
S130	0	@ad<"20000919" and ((ternary adj content adj address$4 adj memory) or TCAM or (ternary adj CAM)) same (ALU or ((arithmetic or logic) near unit))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/01/25 19:37
S131	6	@ad<"20000919" and ((ternary adj content adj address$4 adj memory) or TCAM or (ternary adj CAM)) and (ALU or ((arithmetic or logic) near unit))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/01/25 19:37
S132	2	"5408434".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/01/25 21:14
S133	2	"5408434".pn. and CAM and (logic or circuit)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/01/25 21:14